

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	Group Art Unit: 2675
Jun KOYAMA et al.	)	Examiner: K. Kumar
Based on Serial No. 10/341,470	)	
Filed on: January 14, 2003	)	
For: DRIVING CIRCUIT OF A	)	
SEMICONDUCTOR DISPLAY	)	
DEVICE AND THE	)	
SEMICONDUCTOR DISPLAY	)	
DEVICE	)	

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:


In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application.

Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

U.S. Patent Application Serial No. 09/986,742 was cited as a related application in the parent application and has now issued as U.S. Patent Publication No. 2002/0053671.

The references listed on the attached PTO 1449 Form were cited in patent application Serial No. 10/341,470 and predecessor application Serial No. 09/275,426 (now U.S. Patent 6,549,184), and copies of the references can be found in those applications (37 CFR 1.98(d)(1)-(2)).

Respectfully submitted,

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Reg. No. 38,285

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Potomac Falls, Virginia 20165  
(571) 434-6789

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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	Based on 10/341,470
				Filing Date	January 14, 2003
				First Named Inventor	Jun KOYAMA, et al.
				Group Art Unit	2675
Examiner Name	S. Kumar				
Sheet	1	of	4	Attorney Docket Number	0756-7297

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
		5,734,366		Kubota et al.	03/31/1998	
		5,764,206		Koyama et al.	06/09/1998	
		5,956,581		Yamazaki et al.	09/21/1999	
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		5,731,613		Yamazaki et al.	03/24/1998	
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		6,069,605		Ozawa	05/30/2000	
		2002/0053671		Koyama	05/09/2002	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
		JP	08-172049			07/02/1996	Abst.
		EP	0 657 863			06/14/1995	Eng.
		JP	05-001893			01/08/1993	Abst.
		JP	07-162788			06/23/1995	Abst.

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		U.S. Patent Application Serial No. 09/334,646, including specification, drawings, and pending claims, "Semiconductor Device and Fabrication Method Thereof," Shunpei YAMAZAKI, et al., June 17, 1999.		
		Furue et al., "P-78: Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCSD Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," pp. 782-785, 1998, SID 98 Digest.		

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				Examiner Name		S. Kumar	
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		Number	Kind Code <sup>2</sup> (if known)			
		6,246,387		Yamazaki	06/12/2001	
		6,496,171		Yamazaki	12/17/2002	
		5,432,527		Yanai et al.	07/11/1995	
		4,797,629		Widlar	01/10/1989	
		5,594,569		Konuma et al.	01/14/1997	
		5,643,826		Ohtani et al.	07/01/1997	
		5,648,277		Zhang et al.	07/15/1997	
		5,767,529		Kobori et al.	06/16/1998	
		5,844,535		Itoh et al.	12/01/1998	
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		5,942,856		Koyama	08/24/1999	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
		JP	07-130652			05/19/1995	Abst.
		JP	07-135318			05/23/1995	Abst.
		JP	08-078329			03/22/1996	Abst.

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		Specifications and drawings for Application Serial No. 09/300,133, "Thin Film Transistor Circuit and a Semiconductor Display Device Using the Same," Filing Date: April 27, 1999, Inventors: Shunpei YAMAZAKI et al.	
		Yoneda et al., "A Smart Arrangement of TFTs in Low-Temperature Poly-Silicon Circuitry for Achieving Higher Effective Yield in Production," pp. L1-L4, 1997, SID.	
		Ohtani et al., "Late-News Poster: A 60 in. HDTV Rear Projector with Continuous-Grain- Silicon Technology," Society for Information Display International Symposium-Digest of Technical Papers, Vol. XXIX, May 1998, pp. 467-470.	

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Sheet	3	of	4	Attorney Docket Number		0756-7297	

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		5,247,375		Mochizuki et al.	09/21/1993	
		5,365,875		Asai et al.	11/22/1994	
		5,372,836		Imahashi et al.	12/13/1994	
		5,403,762		Takemura	04/04/1995	
		5,413,958		Imahashi et al.	05/09/1995	
		5,432,122		Chae	07/11/1995	
		5,589,406		Kato et al.	12/31/1996	
		5,471,225		Parks	11/28/1995	
		5,597,740		Ito et al.	01/28/1997	
		5,798,742		Watatani et al.	08/25/1998	
		5,808,595		Kubota et al.	09/15/1998	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	05-203977			08/13/93		Full
		JP	04-282869			10/07/92		Abst.
		EP	0 091 806			10/19/83		Eng.
		JP	58-127318			07/29/83		Abst.
		JP	59-161014			09/1984		Abst.

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		Yoshida et al., "33.2: A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," pp. 841-844, 1997, SID 97 Digest.	
		Translation of JP 5-175235, June, 1993.	
		Translation of JP 5-203977, August, 1993.	

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		6,057,183		Koyama et al.	05/02/2000	
		5,250,852		Ovens et al.	10/05/1993	
		5,194,853		Asada	03/16/1993	
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		5,335,023		Edwards	08/02/1994	
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	8-062637			03/08/1996		Abst.
		JP	05-175235			07/13/1993		Full

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		M. Fuse et al., Sol. St. Phenom. 37-38(1994)565, "Performance of poly-si TFTs...", 1994.	
		M. Bonnel et al., "IEEE Electron Dev. Lett., 14(12)(1993)551, "TFTs with two step annealing process," December, 1993.	
		I. Fujieda et al., IEMD '93 Proc., p. 587, "Self-referenced Polys-Si TFT amplifier," 1993.	
		Webster's II New Riverside University Dictionary, Houghton-Mifflin Company, 1994.	

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